



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Erland et al
Appl. No.: 09/917 419
Filed: July 30, 2001
Title: Integrated Power Circuits

Art Unit: 2814
Examiner: Cao
Docket: TI-31306

Assistant Commissioner
for Patents
Box NonFee Amendment
Washington, DC 20231

MAILING CERTIFICATE	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 today.	
<i>Gracia Sansom</i> Gracia Sansom	<i>2-5-03</i> Date

AMENDMENT

Dear Sir:

In response to the Office Action mailed 11/05/2002, please amend as shown on the attached sheets: "Version with markings to show changes made" and "Clean copy".

REMARKS

Claims 1-3 and 5-29 are pending in the application with claims 6-26 allowed, claims 27-29 withdrawn from consideration, and original claims 1-5 amended. Reexamination and reconsideration are hereby requested.

Claims 1-4 were rejected as anticipated by Williams.

Applicants reply that amended independent claims 1 and 3 require a metallization layer between the contact pads and the active devices; whereas, Williams has pad 91 directly on the source contacts. The transistor of Williams has vertical current flow and does not need the metallization layer of the amended claims.

Claim 5 was rejected as unpatentable over Sato.

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